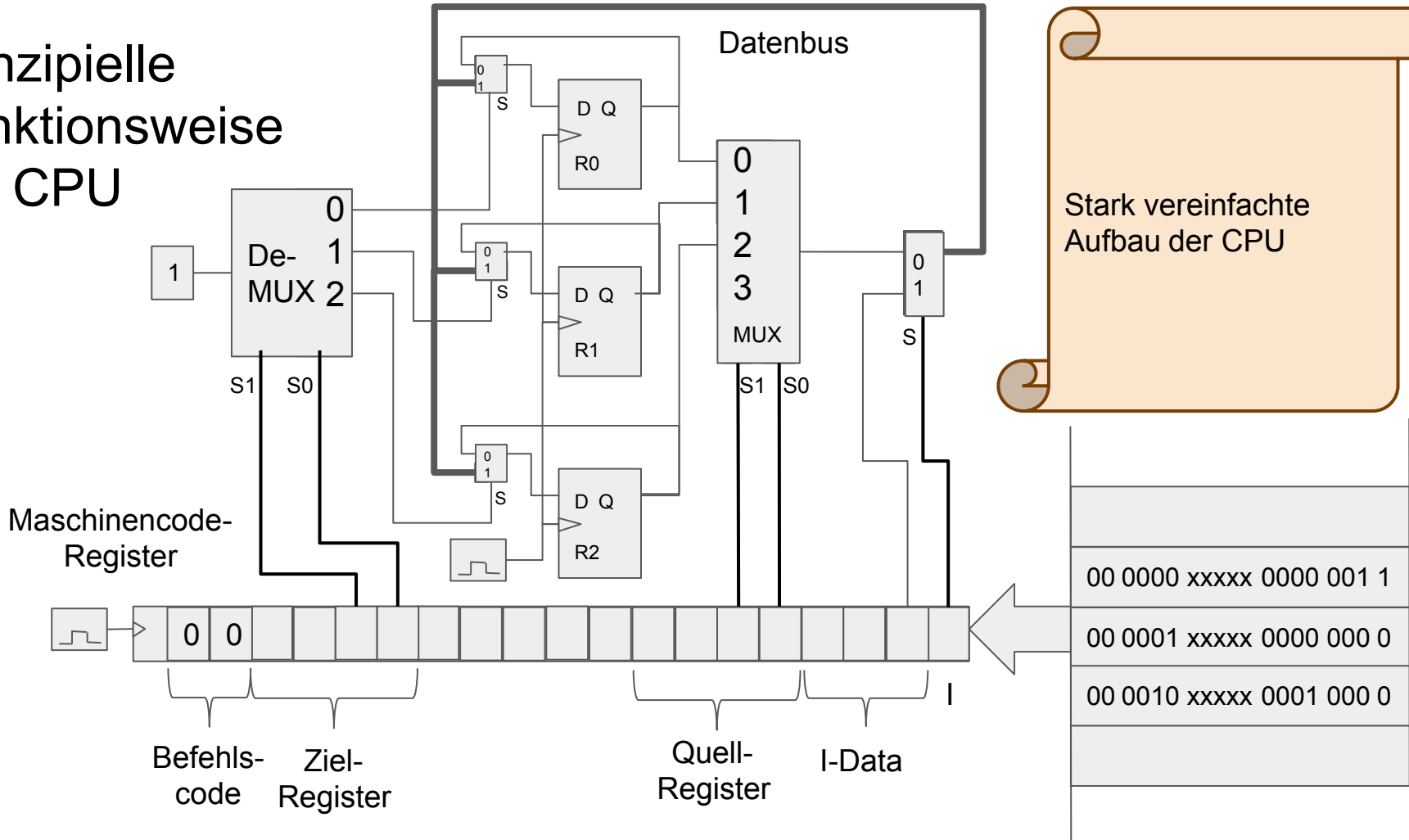
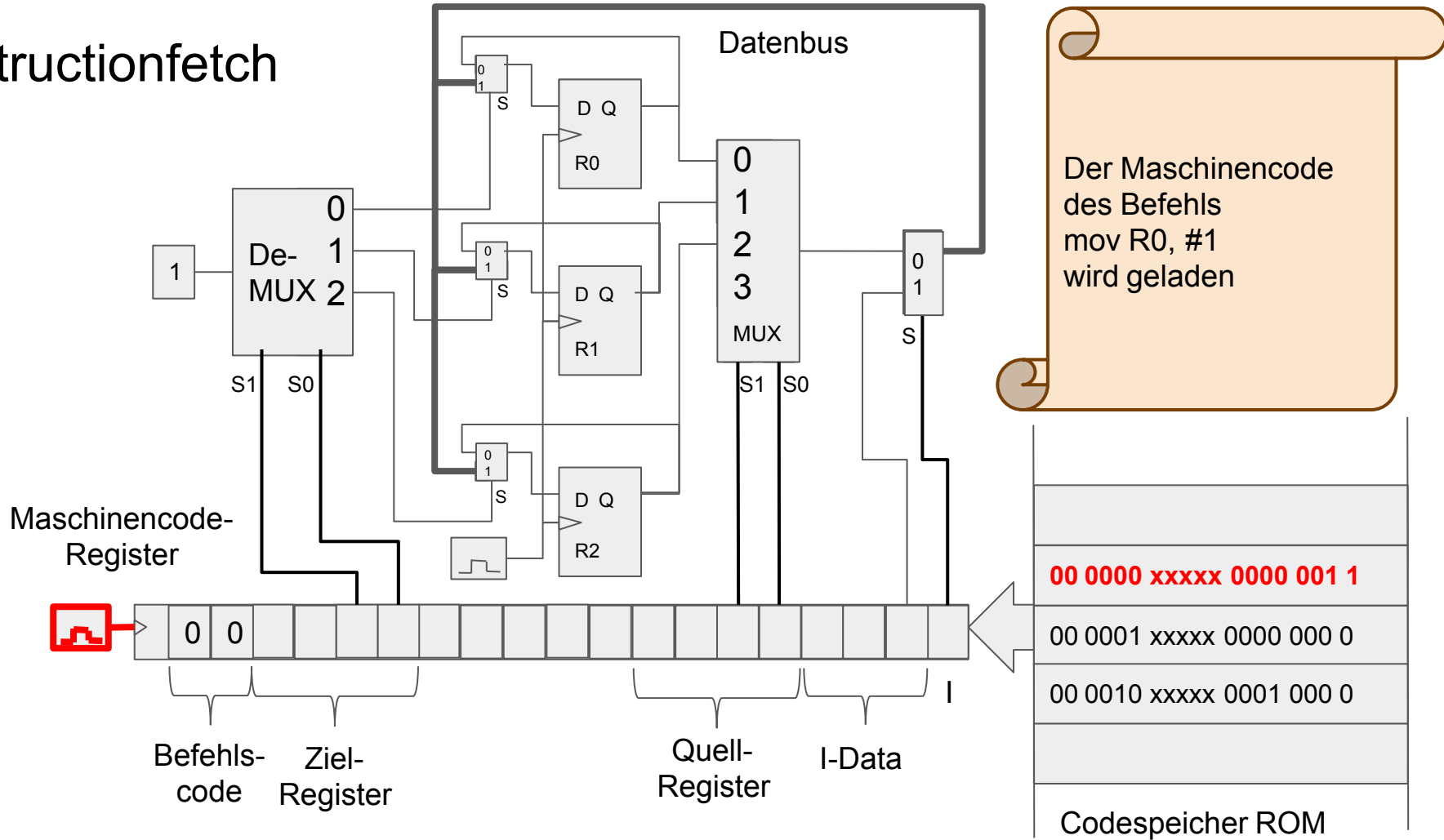


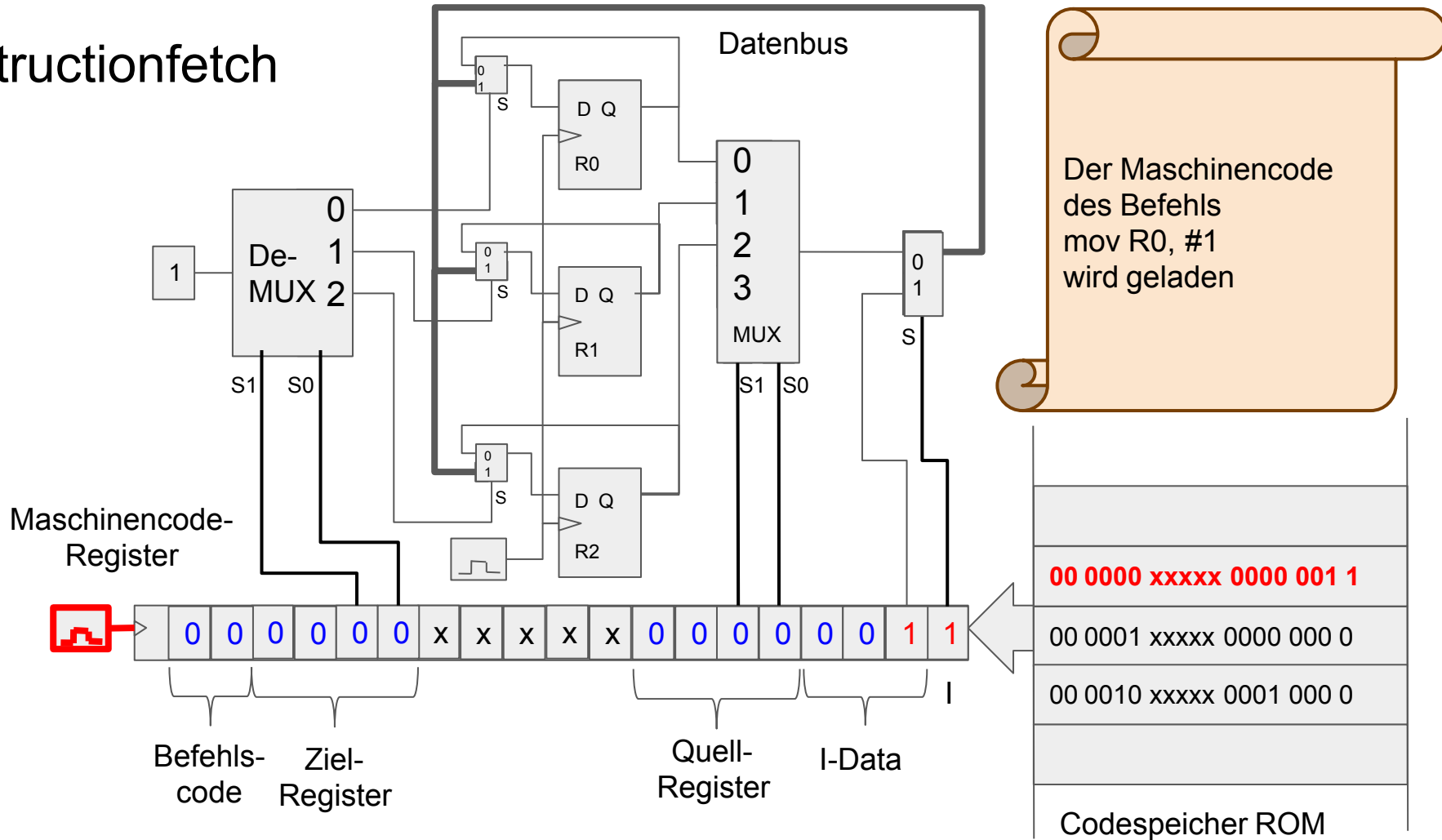
# Prinzipielle Funktionsweise der CPU



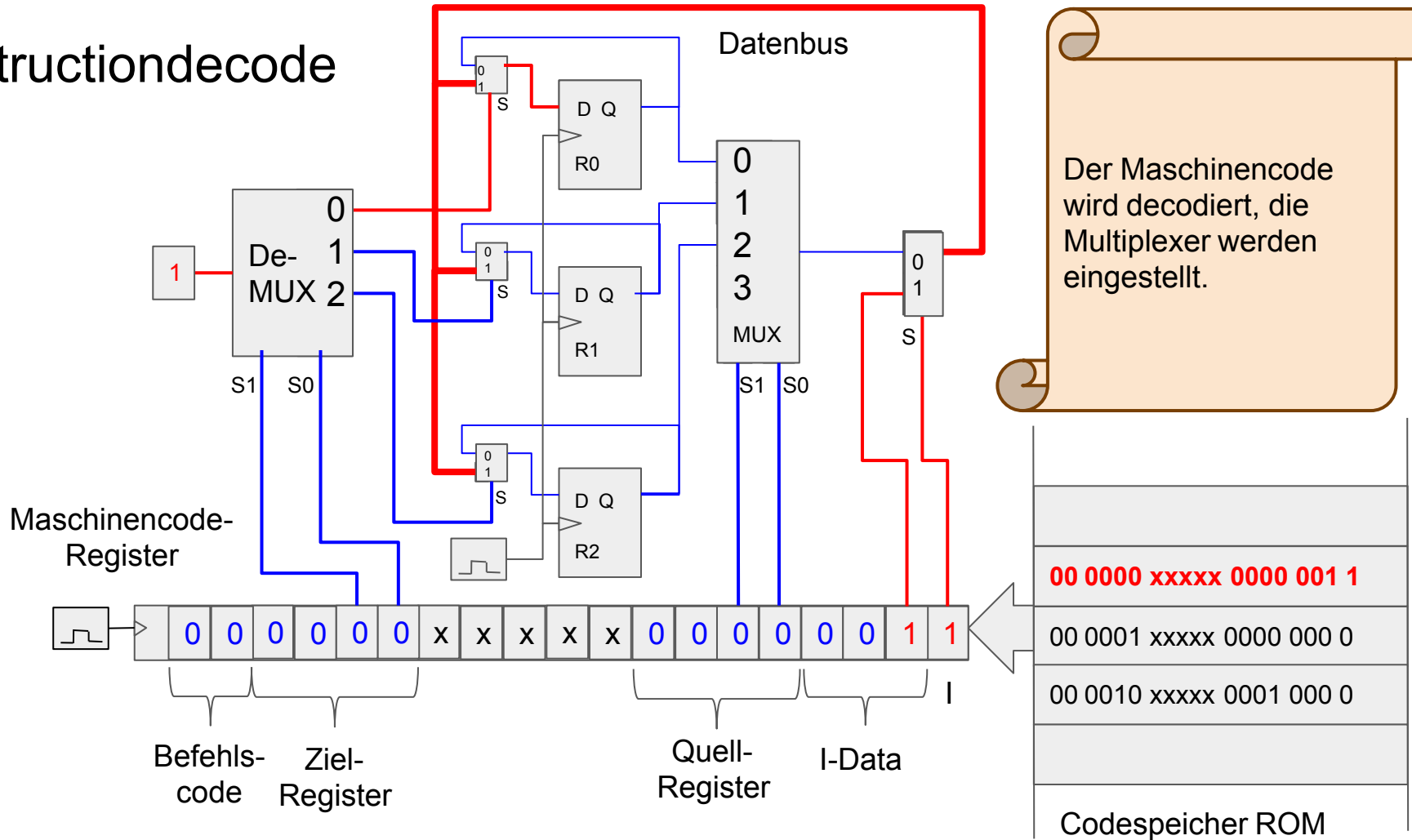
# Instructionfetch



# Instructionfetch

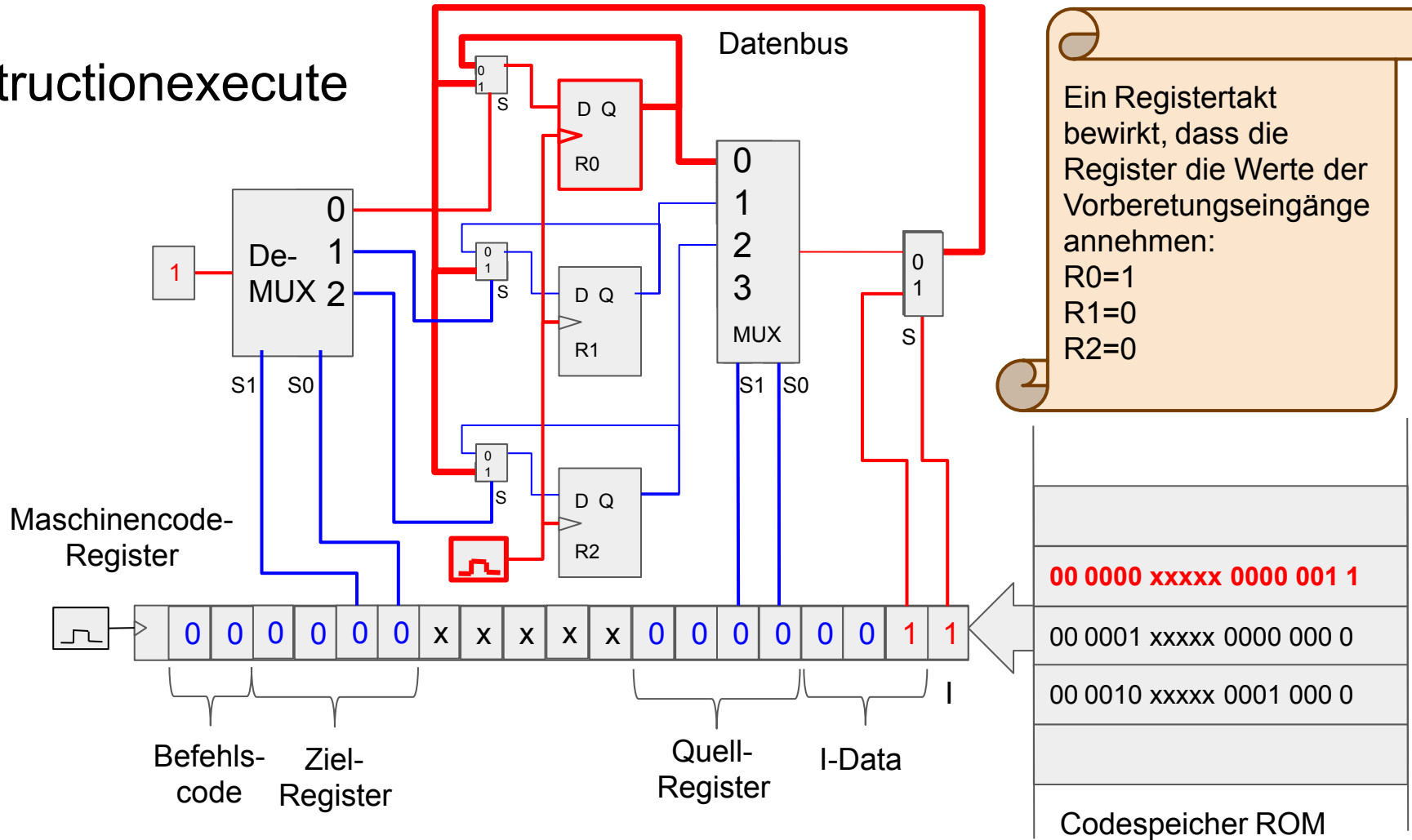


# Instructiondecode

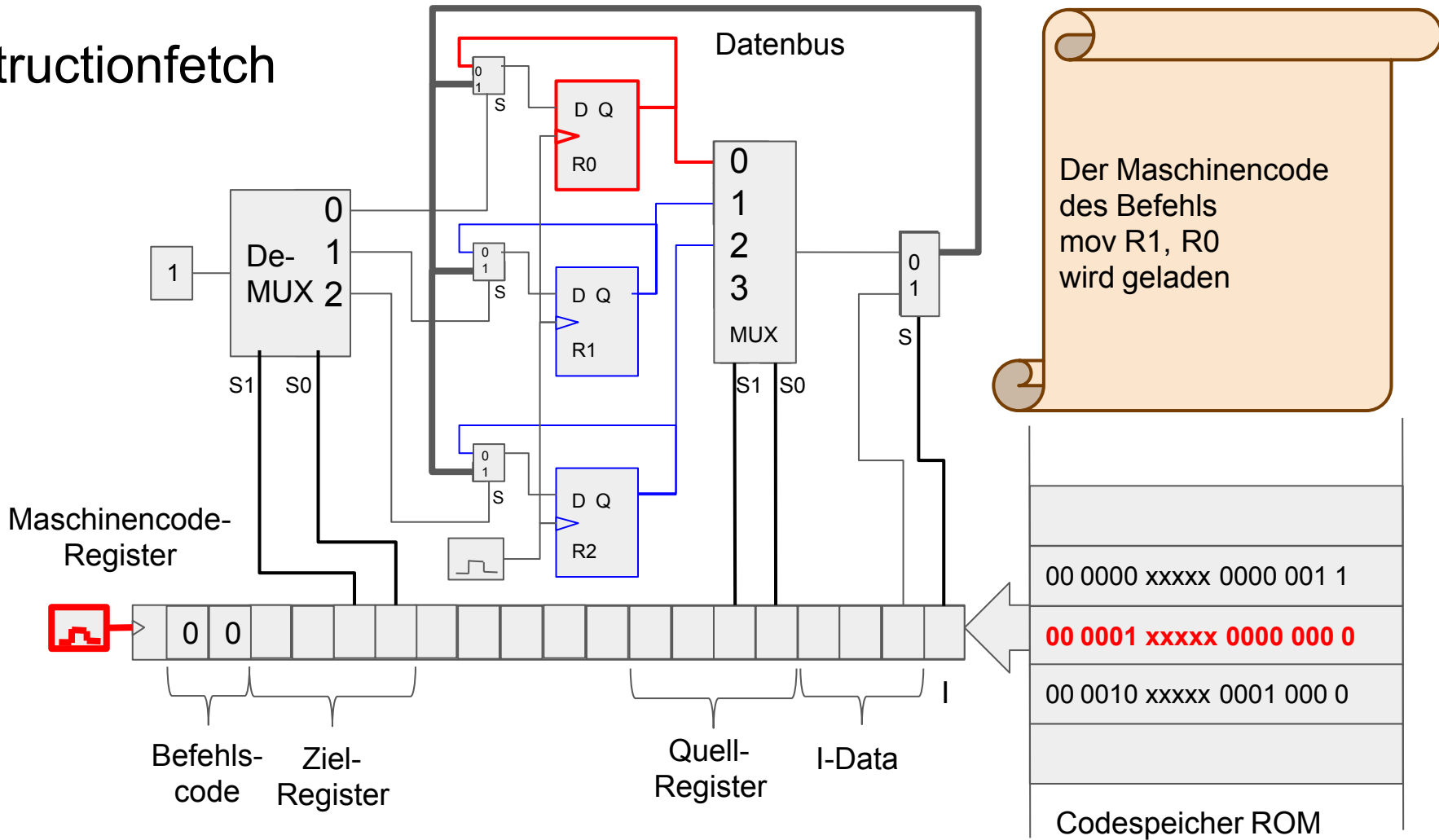


Der Maschinencode wird decodiert, die Multiplexer werden eingestellt.

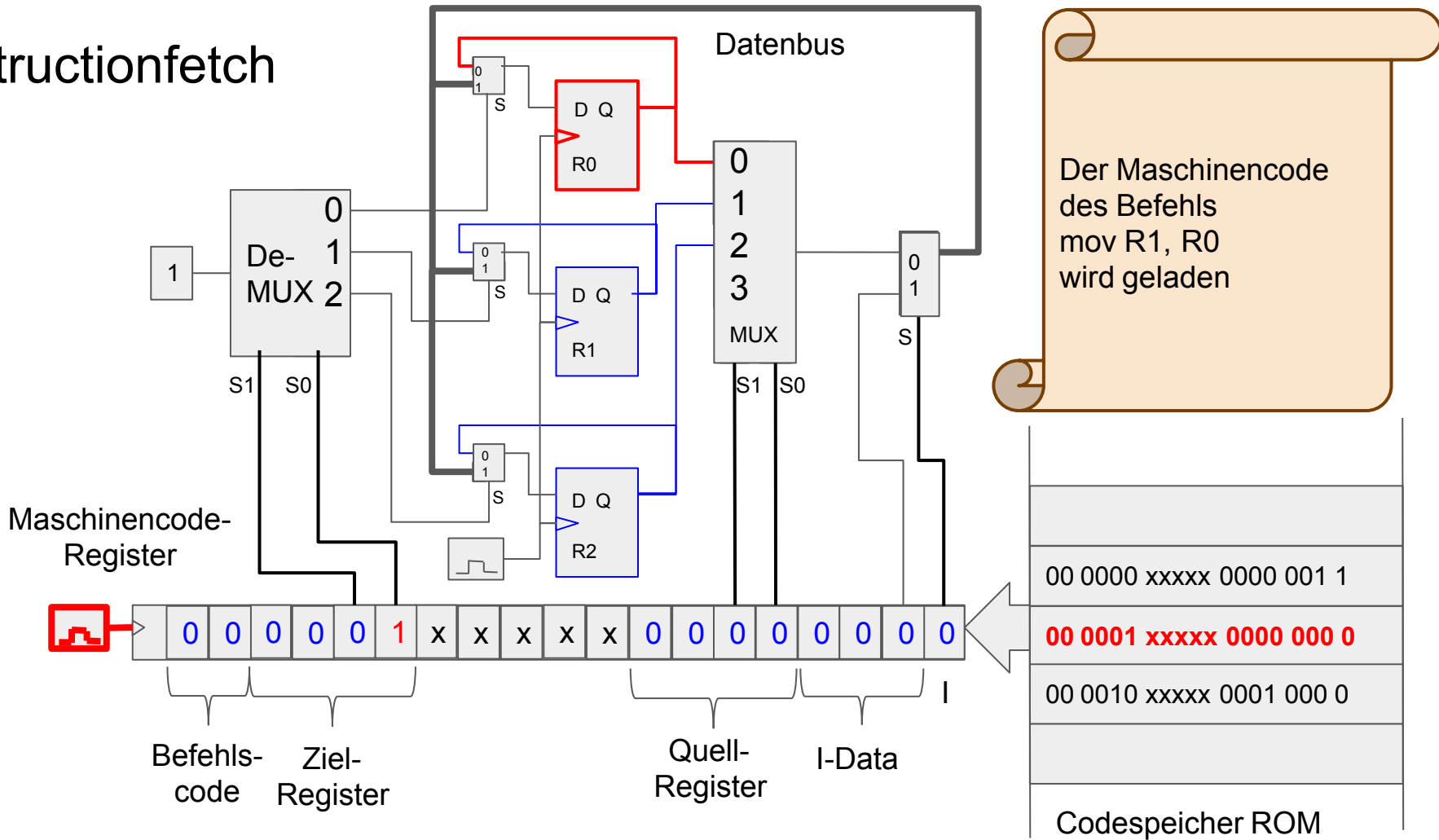
# Instructionexecute



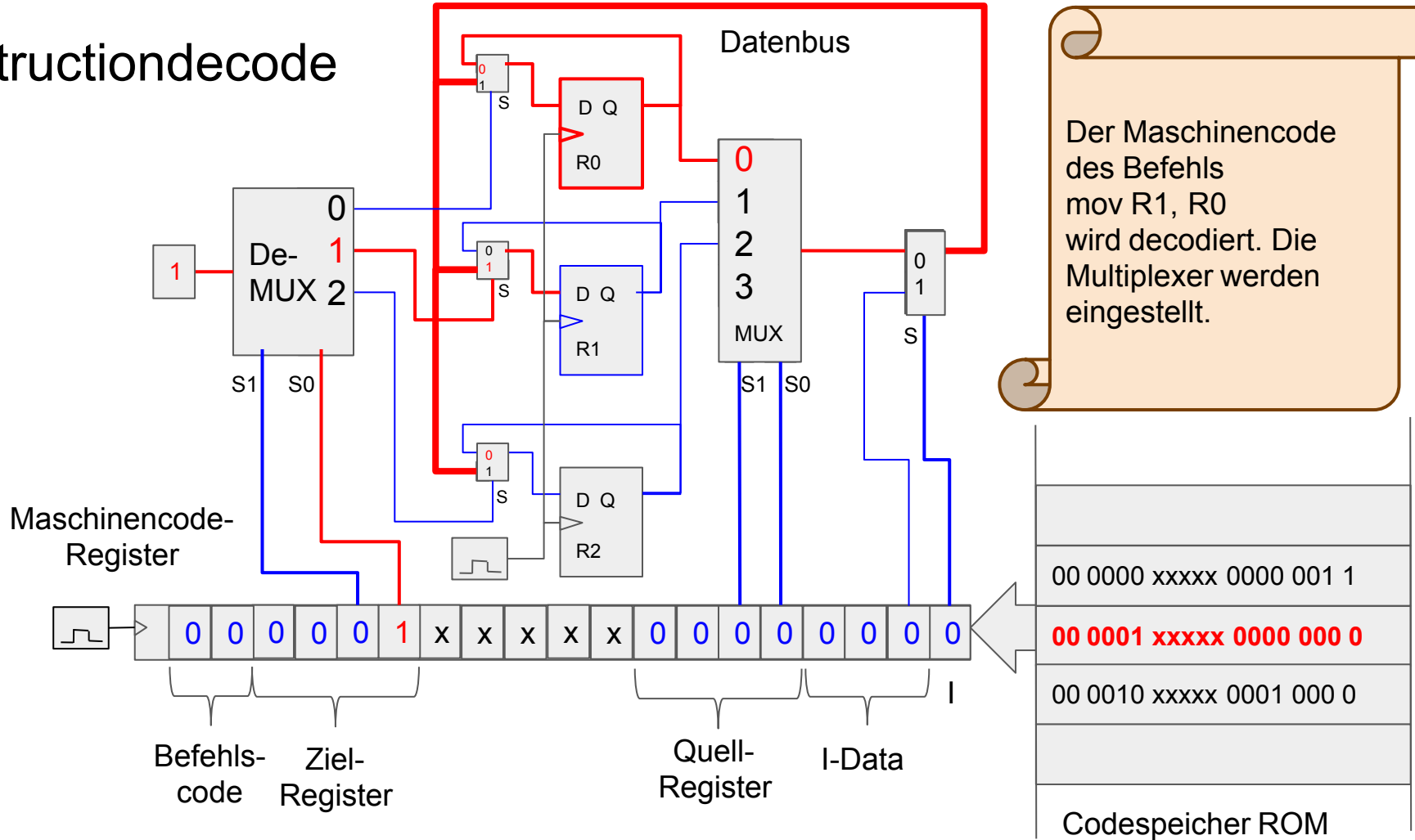
# Instructionfetch



# Instructionfetch



# Instructiondecode





# Instructionexecute

